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AMENDMENTS TO THE CLAIMS

1. (Canceled)

2. (Withdrawn) The image coding device according to claim 1, wherein said coding method is

the MPEG 2.

3. (Withdrawn) The image coding device according to claim 1, wherein said input image

controller excludes either one field of each of the frames at a predetermined rate when the input

motion image signal is of the interlace mode, and outputs the processed motion image signal

together with the process information showing the thus excluded fields, and

said image coder detects the exclusion of fields on the basis of said process information,

and adds an instruction to output another field instead of the excluded field at the time of

decoding as overhead information before coding said processed motion image signal.

4. (Withdrawn) The image coding device according to claim 1, wherein said input image

controller excludes frames at a predetermined rate when the input motion image signal is of the

progressive mode, and outputs the processed motion image signal together with the process

information showing the thus excluded frames, and

said image coder detects the exclusion of frames on the basis of said process information,

and adds an instruction to output another frame field instead of the excluded frame at the time of

decoding as overhead information before coding said processed motion image signal.

5. (Currently Amended) The image coding device according to claim 1 An image coding device for coding a motion image signal, and outputting the coded data as a bit stream, said image coding device comprising:

an input image controller that executes a predetermined processing to an input motion image signal for reducing the amount of coded data when coding the motion image signal by a coding method in which it is prescribed that the frame rate of a motion image signal is set to a constant level to be output at its decoding time, and outputs the processed motion image signal, together with the process information indicating the detail of said processing; and

an image coder for coding the motion image signal processed at said input image controller into a data in conformity with said coding method on the basis of said process information;

wherein said input image controller excludes either one field of each of the frames at a predetermined rate when the input motion image signal is of the interlace method, and outputs the processed motion image signal, together with the process information showing the thus excluded fields, and

said image coder detects the exclusion of fields on the basis of said process information, performs a predictive coding with respect to the thus excluded fields on the basis of the other fields, and generates a coded data corresponding to said excluded fields.

6. (Currently Amended) The image coding device according to claim 5, An image coding device for coding a motion image signal, and outputting the coded data as a bit stream, said image coding device comprising:

an input image controller that executes a predetermined processing to an input motion

image signal for reducing the amount of coded data when coding the motion image signal by a

coding method in which it is prescribed that the frame rate of a motion image signal is set to a

constant level to be output at its decoding time, and outputs the processed motion image signal,

together with the process information indicating the detail of said processing; and

an image coder for coding the motion image signal processed at said input image controller

into a data in conformity with said coding method on the basis of said process information;

wherein said input image controller excludes either one field of each of the frames at a

predetermined rate when the input motion image signal is of the interlace method, and outputs

the processed motion image signal, together with the process information showing the thus

excluded fields;

said image coder detects the exclusion of fields on the basis of said process information,

performs a predictive coding with respect to the thus excluded fields on the basis of the other

fields, and generates a coded data corresponding to said excluded fields; and

wherein said image coder executes a predictive coding, considering all the motion vectors

in the horizontal and vertical directions to be "0".

7. (Currently Amended) The image coding device according to claim 5, An image coding device

for coding a motion image signal, and outputting the coded data as a bit stream, said image

coding device comprising:

an input image controller that executes a predetermined processing to an input motion

image signal for reducing the amount of coded data when coding the motion image signal by a

coding method in which it is prescribed that the frame rate of a motion image signal is set to a

constant level to be output at its decoding time, and outputs the processed motion image signal,

together with the process information indicating the detail of said processing; and

an image coder for coding the motion image signal processed at said input image controller

into a data in conformity with said coding method on the basis of said process information;

wherein said input image controller excludes either one field of each of the frames at a

predetermined rate when the input motion image signal is of the interlace method, and outputs

the processed motion image signal, together with the process information showing the thus

excluded fields;

said image coder detects the exclusion of fields on the basis of said process information,

performs a predictive coding with respect to the thus excluded fields on the basis of the other

fields, and generates a coded data corresponding to said excluded fields; and

wherein-said image coder executes a predictive coding, considering all the motion vectors

in the horizontal direction to be "0", and all the motion vectors in the vertical direction to be

either "+0.5" or "-0.5".

8. (Currently Amended) The image coding device according to claim 5, wherein An image

coding device for coding a motion image signal, and outputting the coded data as a bit stream,

said image coding device comprising:

an input image controller that executes a predetermined processing to an input motion

image signal for reducing the amount of coded data when coding the motion image signal by a

coding method in which it is prescribed that the frame rate of a motion image signal is set to a

constant level to be output at its decoding time, and outputs the processed motion image signal,

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together with the process information indicating the detail of said processing; and

an image coder for coding the motion image signal processed at said input image controller

into a data in conformity with said coding method on the basis of said process information;

wherein said input image controller excludes either one field of each of the frames at a

predetermined rate when the input motion image signal is of the interlace method, and outputs

the processed motion image signal, together with the process information showing the thus

excluded fields,

said image coder detects the exclusion of fields on the basis of said process information,

performs a predictive coding with respect to the thus excluded fields on the basis of the other

fields, and generates a coded data corresponding to said excluded fields;

either said image coder or said input image controller observes a motion vector from two

fields; namely preceding and following fields of each of said excluded fields, and

said image coder interpolates said determined motion vector in accordance with each of

the intervals between each of said excluded fields and said two fields, and executes a predictive

coding about said excluded fields.

9. (Withdrawn) The image coding device according to claim 1, wherein said image coder

changes the size of a motion image signal, outputs the motion image signal after processing, and

also outputs process information indicating that the image size has been changed, and

said image coder detects frames whose image sizes have been changed within the whole

processed motion image signal on the basis of the process information, and performs an

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intra-coding operation with respect to the first coded frame.

10. (Withdrawn) The image coding device according to claim 9, wherein said image coder

changes the size of a motion image signal at the top of every predetermined image units, and

said image coder performs an intra-coding operation with respect to the frame whose image

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size has been changed.

11. (Withdrawn) The image coding device according to claim 10, wherein said input image

controller changes the size of a frame immediately after a GOP header.

12. (Withdrawn) The image coding device according to claim 1, wherein said input image

controller suppresses high-frequency components of a motion image signal.

13. (Withdrawn) The image coding device according to claim 1, wherein said input image

controller executes a predetermined processing in accordance with the picture mode of each

frame.

14. (Withdrawn) The image coding device according to claim 1, wherein said input image

controller determines the amount of coding at the image coder on the basis of at least either one

of the discrete state of pixel values in each frame, the difference of pixels between frames and a

motion vector, and executes a predetermined processing in accordance with the thus determined

amount of coding.

15. (Withdrawn) The image coding device according to claim 1, wherein said input image

controller determines the amount of coding at the image coder on the basis of the mode of

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pictured scene, and executes a predetermined processing in accordance with the thus determined

mode of pictured scene.

16. (Original) A method of image coding for coding a motion image signal and outputting the

thus coded data as a bit stream, said method comprising the steps of:

executing a predetermined processing to an input motion image signal for reducing the

amount of coded data when coding the motion image signal by a coding method in which it is

prescribed that the frame rate of a motion image signal is set to a constant level to be output at its

decoding time, and outputting the processed motion image signal, together with the process

information indicating the detail of said processing, and

coding the motion image signal processed at said input image controller into a data in

conformity with said coding method on the basis of said process information.

17. (Original) An image coding device for coding a motion image signal and outputting the thus

coded data as a bit stream, said device comprising:

an input image controller which, in the case where the motion image signal is of the

interlace mode, equalizes two fields of each frame at a predetermined rate, and outputs the thus

processed motion image signal, and

an image coder that codes the motion image signal processed at said input image

controller by a decoding method which is in conformity with a method that is designed for

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outputting a motion image signal at a constant frame rate.

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Amendment dated March 9, 2006

After Final Office Action of December 9, 2005

18. (Previously Presented) The image coding device according to claim 1, wherein the input

image controller executes the predetermined processing to the motion image signal when the

frame rate of the motion image signal is greater than a predetermined frame rate.

19. (Previously Presented) The image coding device according to claim 1, wherein the

predetermined processing that is executed by the input image controller includes excluding a

field or frame of the motion image signal such that the frame rate of the motion image signal is

reduced to a predetermined frame rate.

20. (Previously Presented) The image coding device according to claim 19, wherein the process

information indicates which fields or frames have been excluded.

21. (Previously Presented) The image coding device according to claim 16, wherein the input

image controller executes the predetermined processing to the motion image signal when the

frame rate of the motion image signal is greater than a predetermined frame rate.

22. (Previously Presented) The image coding device according to claim 16, wherein the

predetermined processing that is executed by the input image controller includes excluding a

field or frame of the motion image signal such that the frame rate of the motion image signal is

reduced to a predetermined frame rate.

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23. (Previously Presented) The image coding device according to 22, wherein the process

information indicates which fields or frames have been excluded.

24. (Previously Presented) The image coding device according to claim 17, wherein the input

image controller executes the predetermined processing to the motion image signal when the

frame rate of the motion image signal is greater than a predetermined frame rate.

25. (Previously Presented) The image coding device according to claim 17, wherein the

predetermined processing that is executed by the input image controller includes excluding a

field or frame of the motion image signal such that the frame rate of the motion image signal is

reduced to a predetermined frame rate.

26. (Previously Presented) The image coding device according to 25, wherein the process

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information indicates which fields or frames have been excluded.